

Method and system using hardware assistance for instruction tracing
by revealing executed opcode or instruction

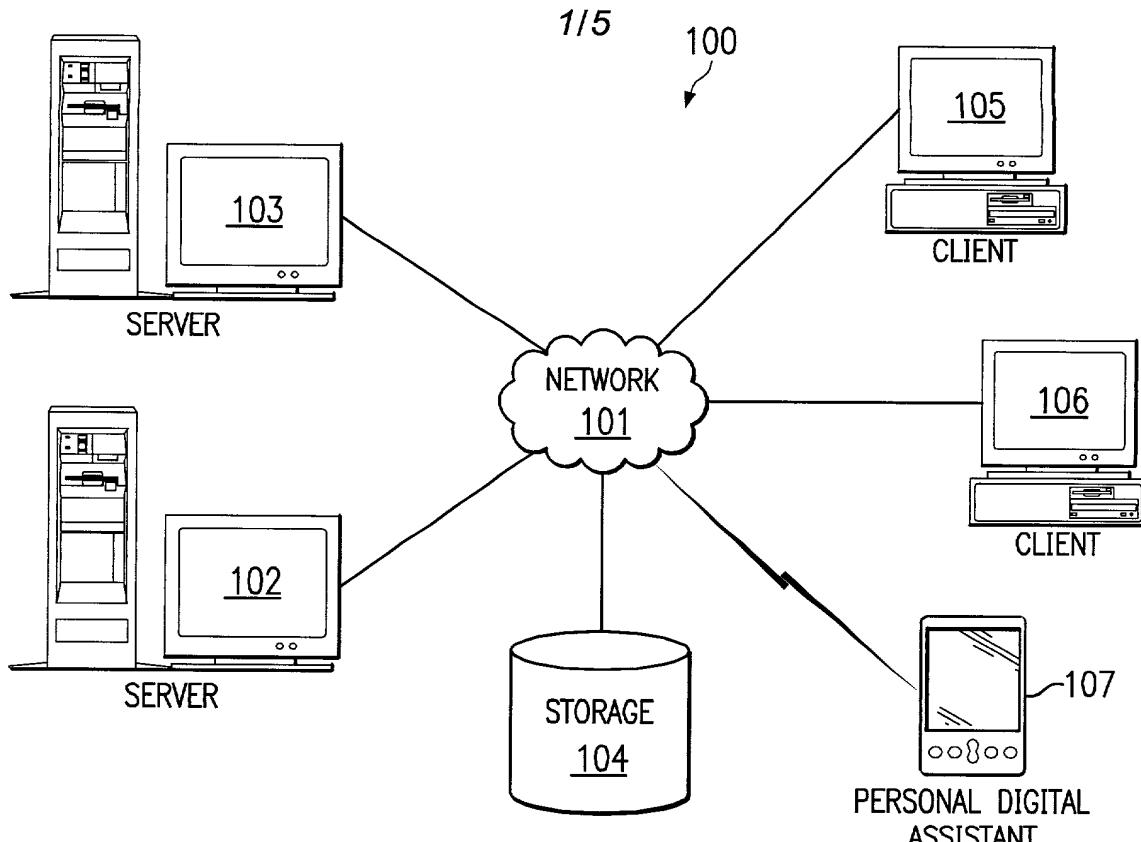


FIG. 1A
(PRIOR ART)

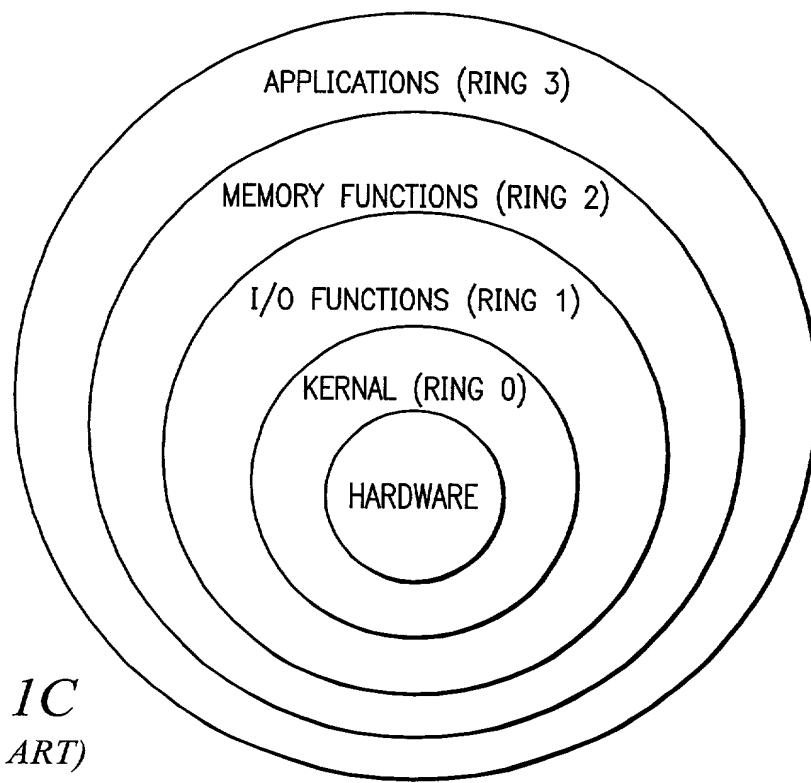


FIG. 1C
(PRIOR ART)

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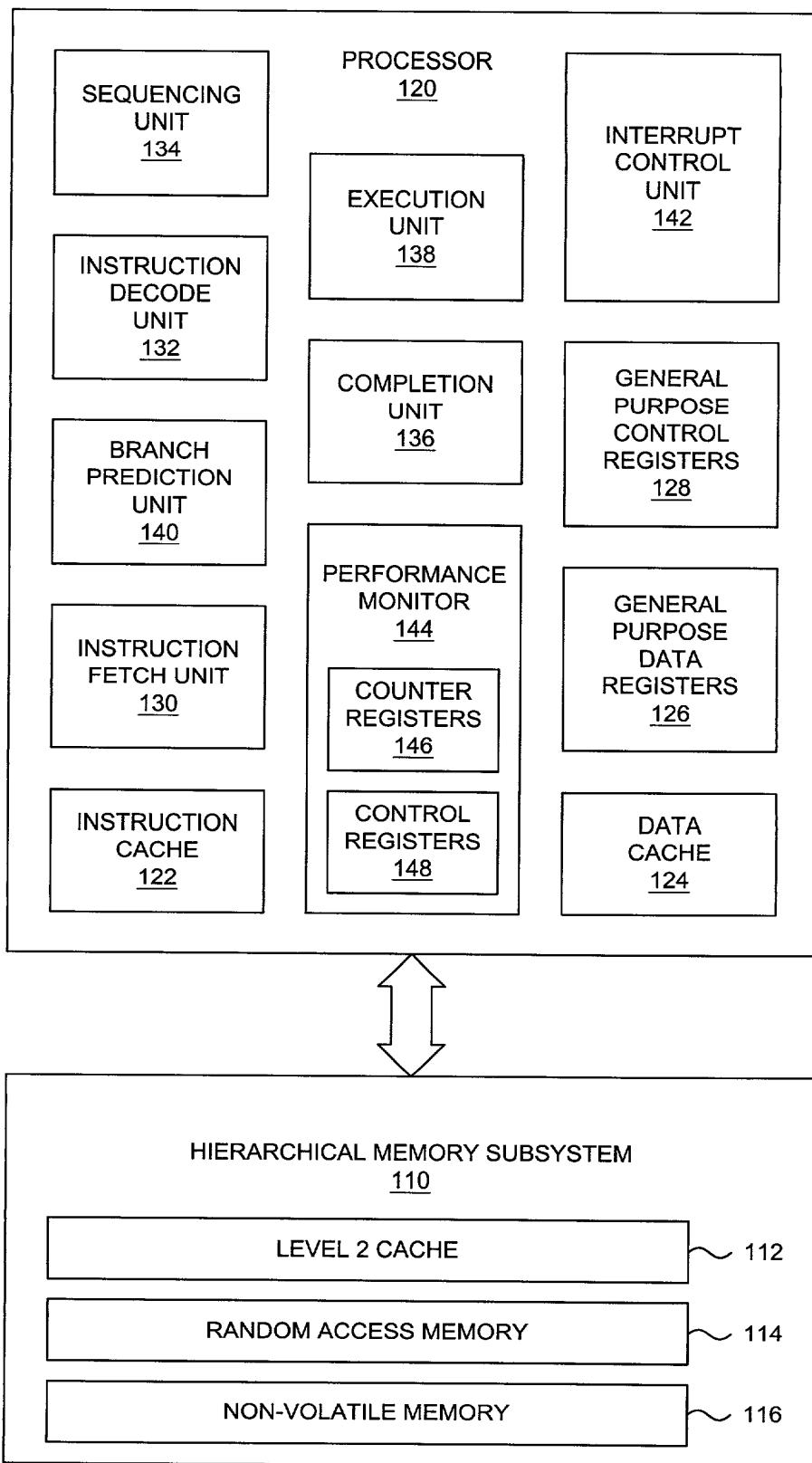


FIG. 1B
(PRIOR ART)

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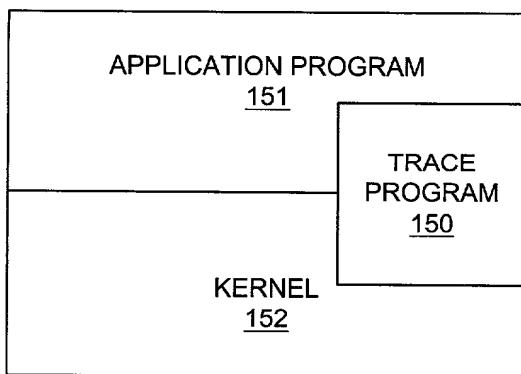


FIG. 1D
(PRIOR ART)

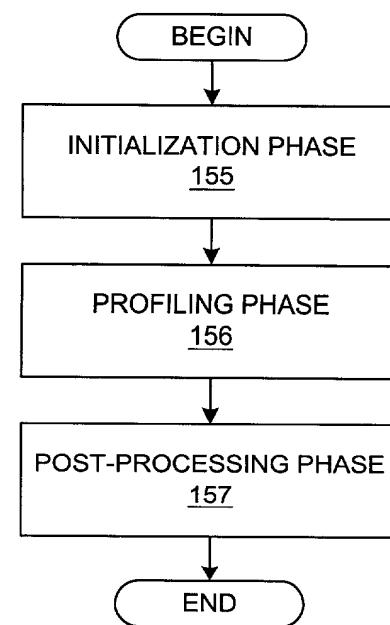


FIG. 1E
(PRIOR ART)

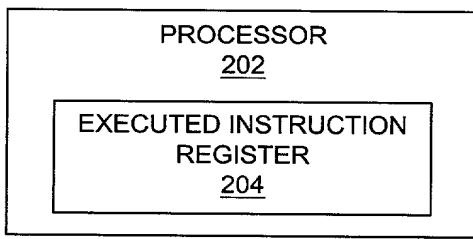


FIG. 2A

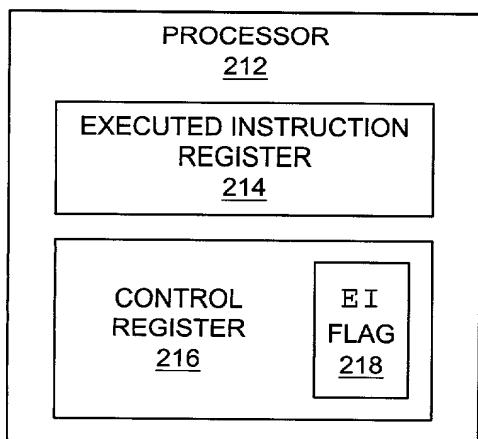


FIG. 2B

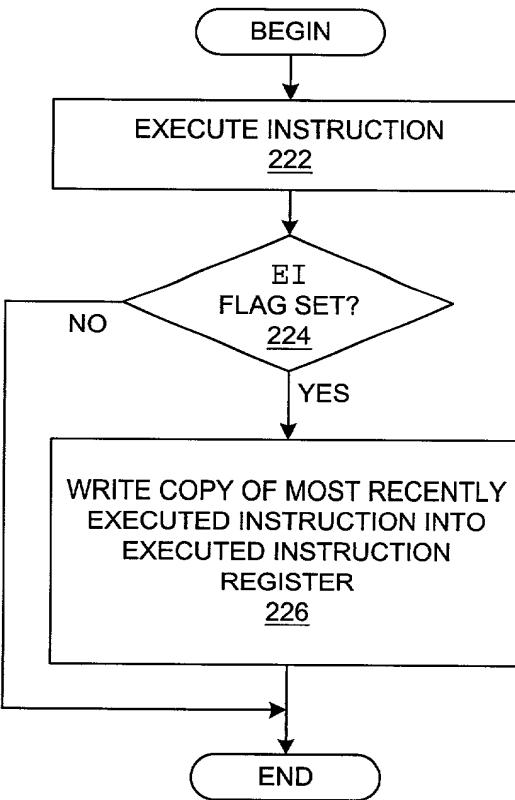


FIG. 2C

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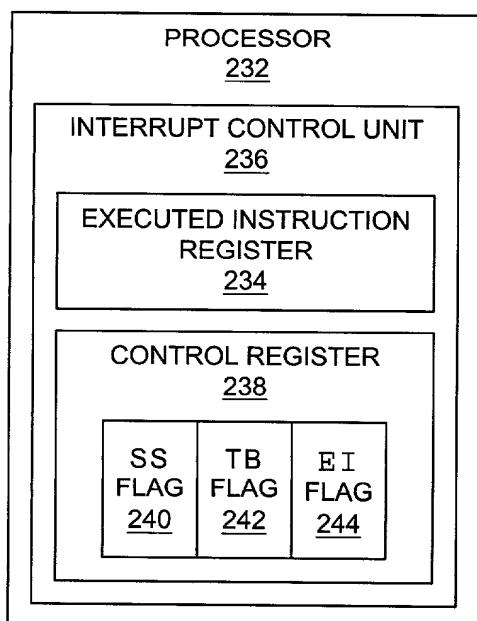


FIG. 2D

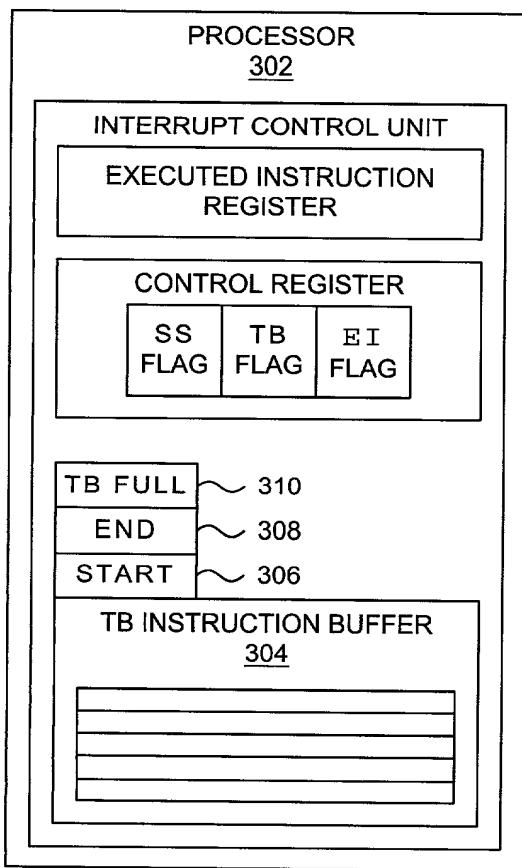


FIG. 3A

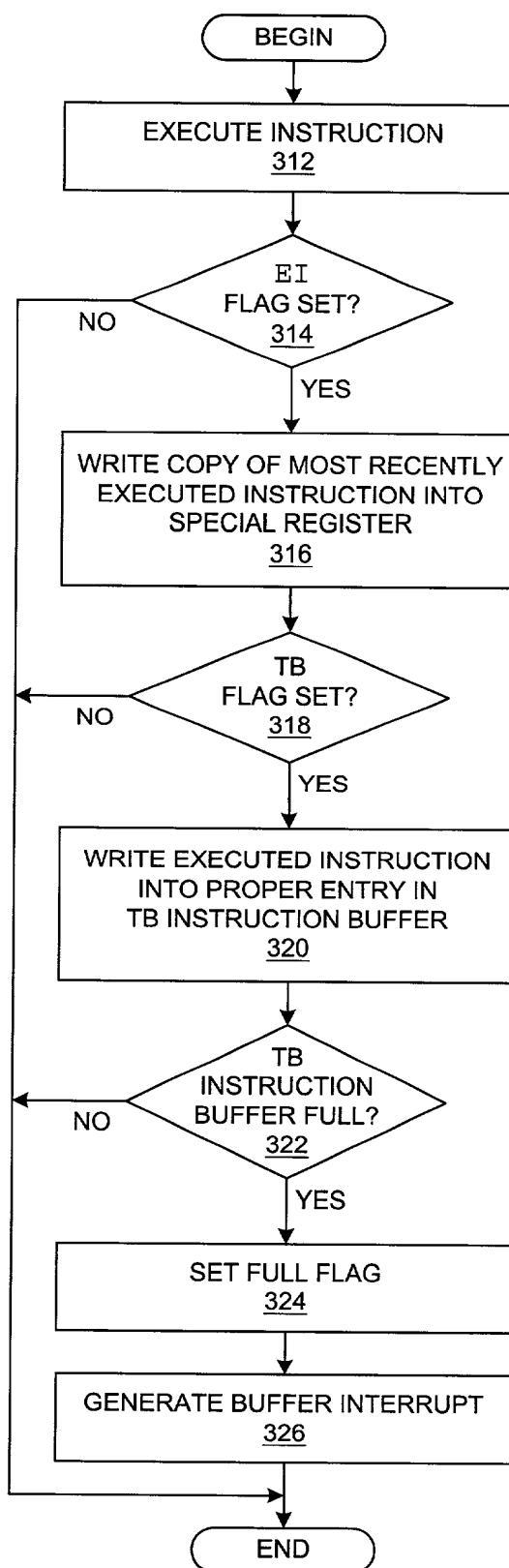


FIG. 3B

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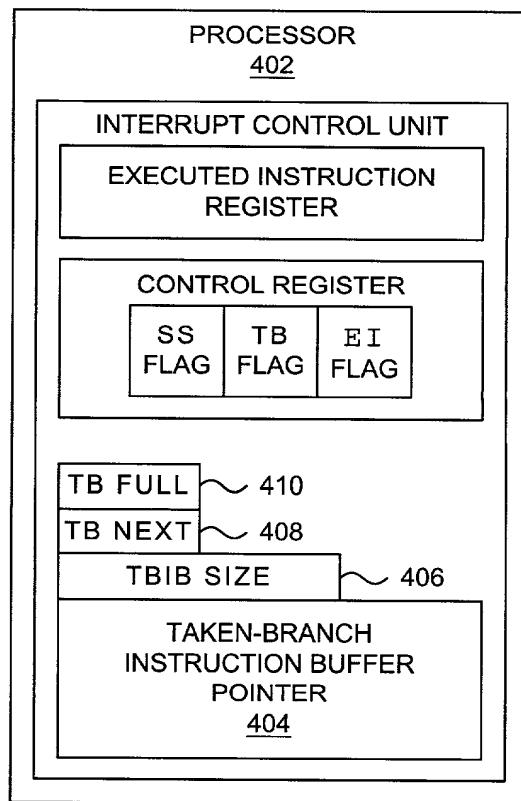


FIG. 4